

Figure 1

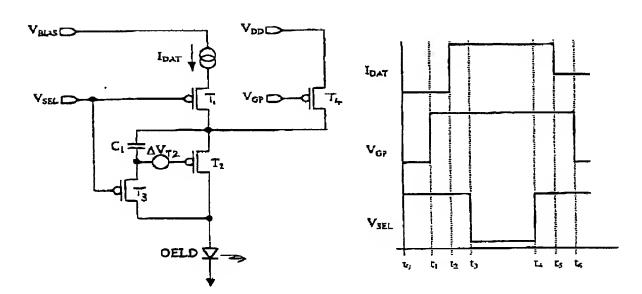


Figure 2A

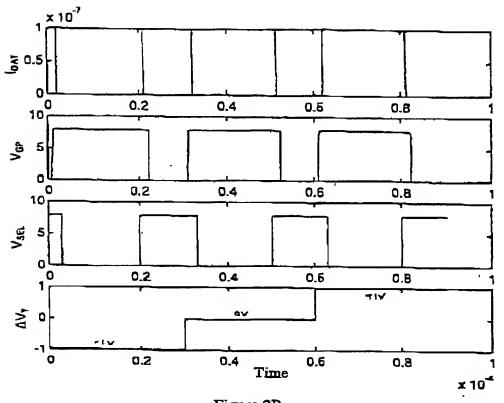


Figure 2B

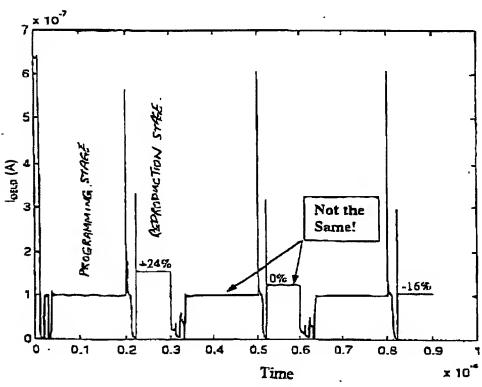


Figure 2C

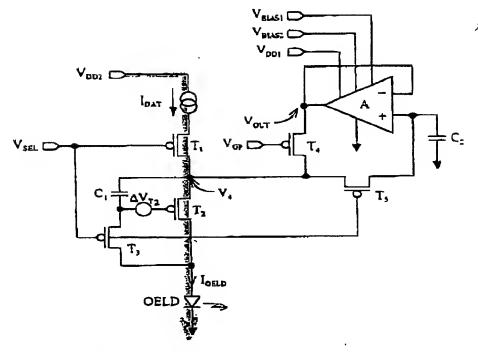


Figure 3

Open-Loop Gain (OLG)	>20
V _{DQ}	9 V
V ₅₅	0 V
Voca	1 V - 8 V
Inverting Input (V)	⇒ V _{otrt}
Non-Inverting Input (V_)	1 V - 8 V
Max Output Current	1uA
Senle Time	<20 µs
Offset Voltage	Minimal
Quiescent Current	Minimal
Space Occupation	<210×70 µm

Single Power Supply Rail to Rail

Figure 4

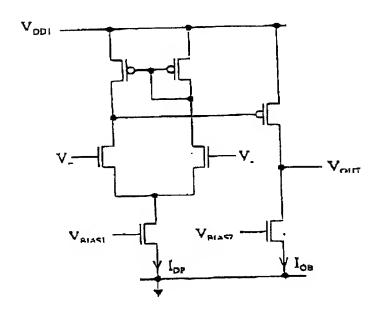


Figure 5

